

**Amendments to the Specification:**

Please amend the specification as follows:

Please replace paragraph starting at page 4, line 12, with the following rewritten paragraph:

~~FIG. 5 is~~FIGS. 5a-5e are an electrical circuit diagram in schematic form of the transceiver circuitry, according to the preferred embodiment of the present invention.

Please replace the paragraph starting at page 5, line 18, with the following rewritten paragraph:

~~FIG. 5 shows~~FIGS. 5a-5e show the electrical circuitry of the trainable transceiver 10 in schematic form. The electrical circuit schematic may be separated into seven primary components: power circuitry 32; user interface circuitry 34; a controller/microprocessor 36 and its associated circuitry which is used to execute the training, coarse tuning, fine tuning, and power level control software routines to be described later; a transceiver applications specific integrated circuit (ASIC) 38 and its associated circuitry; a voltage controlled oscillator (VCO) 40; antenna tuning circuitry 42; a plurality of antennas 44; and power level sense or detector circuitry 46.